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Ghose, K.; Vasek, P.;
<u>EUROMICRO 96. 'Beyond 2000: Hardware and Software Design Strategies'., I the 22nd EUROMICRO Conference</u>
2-5 Sept. 1996 Page(s):606 - 613
Digital Object Identifier 10.1109/EURMIC.1996.546488

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Jonsson, J.; Olsson, A.; Vasell, J.;
<u>Real-Time Systems, 1995. Proceedings., Seventh Euromicro Workshop on</u>
14-16 June 1995 Page(s):270 - 275
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Ferscha, A.; Johnson, J.;
<u>System Sciences, 1995. Proceedings of the Twenty-Eighth Hawaii Internationa</u>
Volume 2, 3-6 Jan. 1995 Page(s):276 - 285 vol.2
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☐ **4. Efficient collective data distribution in all-port wormhole-routed hypercul**
Robinson, D.F.; Judd, D.; McKinley, P.K.; Cheng, B.C.;
<u>Supercomputing '93. Proceedings</u>
15-19 Nov. 1993 Page(s):792 - 801
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1 SASI enforcement of security policies: a retrospective   ◼

Úlfar Erlingsson, Fred B. Schneider
September 1999 **Proceedings of the 1999 workshop on New security paradigms NSPW '99**
Publisher: ACM Press
Full text available: ⬚ pdf(862.14 KB)   Additional Information: full citation, references, citings, index terms

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2 Enforceable security policies   ◼

Fred B. Schneider
February 2000 **ACM Transactions on Information and System Security (TISSEC)**, Volume 3 Issue 1
Publisher: ACM Press

Full text available: ⬚ pdf(148.24 KB)   Additional Information: full citation, abstract, references, citings, index terms, review

A precise characterization is given for the class of security policies enforceable with mechanisms that work by monitoring system execution, and automata are introduced for specifying exactly that class of security policies. Techniques to enforce security policies specified by such automata are also discussed.

Keywords: EM security policies, SASI, inlined reference monitors, proof carrying code, safety properties, security automata, security policies

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3 Software Engineering for Secure Systems (SESS) --- Building Trustworthy   ◼
Applications: A framework for testing security mechanisms for program-based attacks
Ben Breech, Lori Pollock
May 2005 **ACM SIGSOFT Software Engineering Notes , Proceedings of the 2005 workshop on Software engineering for secure systems—building trustworthy applications SESS '05**, Volume 30 Issue 4
Publisher: ACM Press
Full text available: ⬚ pdf(170.09 KB)   Additional Information: full citation, abstract, references, index terms

Program vulnerabilities leave organizations open to malicious attacks that can result in severe damage to company finances, resources, consumer privacy, and data. Engineering applications and systems so that vulnerabilities do not exist would be the best solution, but this strategy may be impractical due to fiscal constraints or inadequate knowledge.

Therefore, a variety of program and system-based solutions have been proposed to deal with vulnerabilities in a manageable way. Unfortunately, prop ...

4   Military applications: Security issues in high level architecture based distributed   ■
      simulation
      Asa Elkins, Jeffery W. Wilson, Denis Gracanin
      December 2001 **Proceedings of the 33nd conference on Winter simulation WSC '01**
      Publisher: IEEE Computer Society
      Full text available: 📄 pdf(262.26 KB)   Additional Information: full citation, abstract, references, index terms

      The United States Department of Defense (DoD) has, over the past several years, emphasized the need to employ simulation based acquisition (SBA) in engineering and development. Distributed simulation introduces an information assurance challenge and details of a simulation must be guarded from unauthorized access. The High Level Architecture (HLA) and its Run-Time Interface (RTI) do not define support of mandatory access controls (MACs) or discretionary access controls (DACs) required to provide ...

5   Compilation and power: Power consumption profile analysis for security attack   ■
      simulation in smart cards at high abstraction level
      K. Rothbart, U. Neffe, Ch. Steger, R. Weiss, E. Rieger, A. Muehlberger
      September 2005 **Proceedings of the 5th ACM international conference on Embedded
                     software EMSOFT '05**
      Publisher: ACM Press
      Full text available: 📄 pdf(273.40 KB)   Additional Information: full citation, abstract, references, index terms

      Smart cards are embedded systems which are used in an increasing number of secure applications. As they store and deal with confidential and secret data many attacks are performed on these cards to reveal this private information. Consequently, the security demands on smart cards are very high. It is mandatory to evaluate the security of the design but this is performed often very late in the design process or when the chip has already been manufactured. This paper presents a hierarchical securi ...

      Keywords: SystemC, analysis, attack, embedded security, fault injection, power profile, simulation, smart card

6   SOAr-DSGrid: Service-Oriented Architecture for Distributed Simulation on the Grid   ■
      Xinjun Chen, Wentong Cai, Stephen J. Turner, Yong Wang
      May 2006 **Proceedings of the 20th Workshop on Principles of Advanced and
                Distributed Simulation PADS '06**
      Publisher: IEEE Computer Society
      Full text available: 📄 pdf(357.77 KB)   Additional Information: full citation, abstract, index terms

      Simulation is a low cost alternative to experimentation on real-world physical systems. Grid technology enables coordinated use of and secure access to distributed computing resources and data sources. The service-oriented architecture (SOA) is an ideal paradigm for next generation computing. The loose coupling among services in the SOA relieves service consumers from detailed knowledge of implementation, implementation language, and execution platform of the services to be consumed. In this pap ...

7   New techniques for security and reliability enhancement in embedded systems: ■
      Current flattening in software and hardware for security applications
      Radu Muresan, Catherine Gebotys
      September 2004 **Proceedings of the 2nd IEEE/ACM/IFIP international conference on
                      Hardware/software codesign and system synthesis CODES+ISSS '04**
      Publisher: ACM Press

Full text available: 🔲 pdf(344.68 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

This paper presents a new current flattening technique applicable in software and hardware. This technique is important in embedded cryptosystems since power analysis attacks (that make use of the current variation dependency on data and program) compromise the security of the system. The technique flattens the current internally by exploiting current consumption differences at the instruction level. Code transformations supporting current variation reductions due to program dependencies are pre ...

Keywords: current flattening, hardware architecture, power analysis attacks

8   <u>Manufacturing applications: production management IV: Database-intensive process simulation at the Y-12 national security complex</u> ■

Reid Kress, Karen Bills, Jack Dixon, Richard Rinehart
December 2006 **Proceedings of the 38th conference on Winter simulation WSC '06**
Publisher: Winter Simulation Conference
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The NNSA's Y-12 National Security Complex is a manufacturing facility operated by BWXT Y-12. Y-12's missions include ensuring the US' nuclear weapons deterrent, storing nuclear materials, and fueling US naval reactors. As a consequence of these missions, Y-12 makes dozens of products, having hundreds of parts, each with many different process steps associated with manufacturing components, building sub-assemblies, or assembling final products. Y-12 also disassembles weapon components to support ...

9   <u>Session 7A: Bounded-concurrent secure multi-party computation with a dishonest majority</u> ■

Rafael Pass
June 2004 **Proceedings of the thirty-sixth annual ACM symposium on Theory of computing STOC '04**
Publisher: ACM Press
Full text available: 🔲 pdf(246.91 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

We show how to securely realize any multi-party functionality in a way that preserves security under an a-priori bounded number of concurrent executions, regardless of the number of corrupted parties. Previous protocols for the above task either rely on set-up assumptions such as a Common Reference String, or require an honest majority. Our constructions are in the plain model and rely on standard intractability assumptions (enhanced trapdoor permutations and collision resistant hash functions). ...

Keywords: concurrent composition, constant-round protocols, secure multi-party computation, simulation-sound zero-knowledge

10   <u>Session 12A: Bounded-concurrent secure two-party computation without setup assumptions</u> ■

Yehuda Lindell
June 2003 **Proceedings of the thirty-fifth annual ACM symposium on Theory of computing STOC '03**
Publisher: ACM Press
Full text available: 🔲 pdf(338.32 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

In this paper we study the feasibility of obtaining protocols for general two-party computation that remain secure under concurrent composition. (A general protocol can be

used for obtaining secure computation of any functionality.) We consider a scenario where *no trusted setup* is assumed (and so, for example, there is no common reference string available to the parties); we call this the "plain model". We present both negative and positive results for this model. Specifically, we show th ...

Keywords: protocol composition, secure computation

11 A network technique to achieve program and data security with nominal communications overhead
J. R. Driscoll, H. N. Srinidhi, T. S. Chesser
November 1986 **Proceedings of 1986 ACM Fall joint computer conference ACM '86**
Publisher: IEEE Computer Society Press
Full text available: pdf(731.27 KB)   Additional Information: full citation, references, index terms

12 On the composition of authenticated Byzantine Agreement
Yehuda Lindell, Anna Lysyanskaya, Tal Rabin
November 2006 **Journal of the ACM (JACM)**, Volume 53 Issue 6
Publisher: ACM Press
Full text available: pdf(415.67 KB)   Additional Information: full citation, abstract, references, index terms

A fundamental problem of distributed computing is that of simulating a secure broadcast channel, within the setting of a point-to-point network. This problem is known as Byzantine Agreement (or Generals) and has been the focus of much research. Lamport et al. [1982] showed that in order to achieve Byzantine Agreement in the plain model, more than two thirds of the participating parties must be honest. They further showed that by augmenting the network with a public-key infrastructure for digital ...

Keywords: Authenticated Byzantine Agreement, lower bounds, protocol composition, randomized protocols

13 Anonymity systems & formal method: Specifying and analyzing security automata using CSP-OZ
David Basin, Ernst-Ruediger Olderog, Paul E. Sevinc
March 2007 **Proceedings of the 2nd ACM symposium on Information, computer and communications security ASIACCS '07**
Publisher: ACM Press
Full text available: pdf(305.81 KB)   Additional Information: full citation, abstract, references, index terms

Security automata are a variant of Büchi automata used to specify security policies that can be enforced by monitoring system execution. In this paper, we propose using CSP-OZ, a specification language combining Communicating Sequential Processes (CSP) and Object-Z (OZ), to specify security automata, formalize their combination with target systems, and analyze the security of the resulting system specifications. We provide theoretical results relating CSP-OZ specifications and security auto ...

Keywords: CSP-OZ, security automata

14 Session 7A: New notions of security: achieving universal composability without trusted setup
Manoj Prabhakaran, Amit Sahai
June 2004 **Proceedings of the thirty-sixth annual ACM symposium on Theory of**

computing STOC '04
Publisher: ACM Press

Full text available: pdf(253.45 KB)   Additional Information: full citation, abstract, references, citings, index terms

We propose a modification to the framework of Universally Composable (UC) security [3]. Our new notion involves comparing the real protocol execution with an ideal execution involving ideal functionalities (just as in UC-security), but allowing the environment and adversary access to some super-polynomial computational power. We argue the meaningfulness of the new notion, which in particular subsumes many of the traditional notions of security. We generalize the Universal Composition theorem of ...

Keywords: environmental security, general composition, generalized environmental security, secure multi-party computation, secure protocols, simulation, universal composability

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15 Workshop on architectural support for security and anti-virus (WASSA): ChipLock: support for secure microarchitectures
Taeho Kgil, Laura Falk, Trevor Mudge
March 2005 **ACM SIGARCH Computer Architecture News**, Volume 33 Issue 1
Publisher: ACM Press
Full text available: pdf(256.52 KB)   Additional Information: full citation, abstract, references, index terms

The increasing need for security has caused system designers to consider placing some security support directly at the hardware level. In fact, this is starting to emerge as an important consideration in processor design, because the performance overhead of supporting security in hardware is usually significantly lower than a complete software solution. In this paper, we investigate integrating some security support into hardware. We show that security support can be added at some acceptable cos ...

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16 Development of processors and communication networks for embedded systems: System design methodologies for a wireless security processing platform
Srivaths Ravi, Anand Raghunathan, Nachiketh Potlapally, Murugan Sankaradass
June 2002 **Proceedings of the 39th conference on Design automation DAC '02**
Publisher: ACM Press

Full text available: pdf(207.37 KB)   Additional Information: full citation, abstract, references, citings, index terms

Security protocols are critical to enabling the growth of a wide range of wireless data services and applications. However, they impose a high computational burden that is mismatched with the modest processing capabilities and battery resources available on wireless clients. Bridging the security processing gap, while retaining sufficient programmability in order to support a wide range of current and future security protocol standards, requires the use of novel system architectures and design m ...

Keywords: 3DES, AES, DES, IPSec, RSA, SSL, decryption, design methodology, embedded system, encryption, handset, performance, platform, security, security processing, system architecture, wireless

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17 Security: SECA: security-enhanced communication architecture
Joel Coburn, Srivaths Ravi, Anand Raghunathan, Srimat Chakradhar
September 2005 **Proceedings of the 2005 international conference on Compilers, architectures and synthesis for embedded systems CASES '05**
Publisher: ACM Press
Full text available: Additional Information:

pdf(396.53 KB)   full citation, abstract, references, index terms

In this work, we propose and investigate the idea of enhancing a System-on-Chip (SoC) communication architecture (the fabric that integrates system components and carries the communication traffic between them) to facilitate higher security. We observe that a wide range of common security attacks are manifested as abnormalities in the system-level communication traffic. Therefore, the communication architecture, with its global system-level visibility, can be used to detect them. The communicati ...

Keywords: AMBA Bus, access control, architecture, attacks, bus, communication, digital rights management (DRM), intrusion detection, security, security-aware design, small embedded systems, system-on-chip (SoC)

18 Frontmatter (TOC, Letters, Election results, Software Reliability Resources!, Computing Curricula 2004 and the Software Engineering Volume SE2004, Software Reuse Research, ICSE 2005 Forward)
July 2005 **ACM SIGSOFT Software Engineering Notes**, Volume 30 Issue 4
Publisher: ACM Press
Full text available: pdf(6.19 MB)   Additional Information: full citation, index terms

19 Session 14A: Concurrent general composition of secure protocols in the timing model
Yael Tauman Kalai, Yehuda Lindell, Manoj Prabhakaran
May 2005 **Proceedings of the thirty-seventh annual ACM symposium on Theory of computing STOC '05**
Publisher: ACM Press
Full text available: pdf(263.20 KB)   Additional Information: full citation, abstract, references, index terms

In the setting of secure multiparty computation, a set of mutually distrustful parties wish to jointly compute some function of their input (i.e., they wish to securely carry out some distributed task). %The joint computation should be such that even In the stand-alone case, it has been shown that *every* efficient function can be securely computed. However, in the setting of concurrent composition, broad impossibility results have been proven for the case where there is no honest majority ...

Keywords: cryptography, protocol composition, secure multiparty computation

20 Architectures for cryptography and security applications: Simulation models for side-channel information leaks
Kris Tiri, Ingrid Verbauwhede
June 2005 **Proceedings of the 42nd annual conference on Design automation DAC '05**
Publisher: ACM Press
Full text available: pdf(244.28 KB)   Additional Information: full citation, abstract, references, index terms

Small, embedded integrated circuits (ICs) such as smart cards are vulnerable to so-called side-channel attacks (SCAs). The attacker can gain information by monitoring the power consumption, execution time, electromagnetic radiation and other information that is leaked by the switching behavior of digital CMOS gates. Ever since power attacks have been introduced in 1999, many countermeasures have been proposed. Often a significant increase in security has been touted. We will show that in order t ...

Keywords: countermeasure, differential power analysis, encryption, security IC, side-channel attack, simulation model, smart card

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1 Ad hoc networks and RFID (work in progress): Improved topology assumptions for   ■
threshold cryptography in mobile ad hoc networks
Giovanni Di Crescenzo, Renwei Ge, Gonzalo R. Arce
November 2005 **Proceedings of the 3rd ACM workshop on Security of ad hoc and sensor networks SASN '05**
Publisher: ACM Press
Full text available: 📄 pdf(281.49 KB)   Additional Information: full citation, abstract, references, index terms

> Mobile Ad Hoc Networks (MANET), due to their lack of physical infrastructures or centralized authorities, pose a number of security challenges to a protocol designer. In particular, several typical application scenarios demand the design of protocols that cannot base their security on the existence of trusted parties or setup information, but rather need to leverage uniquely on assumptions limiting the corrupting power of the adversaries. This naturally defines security design and analysis parad ...

> **Keywords**: distributed cryptography, mobile ad hoc networks, threshold cryptography, threshold signatures

2 RFID & watermarking: Universally composable and forward-secure RFID   ■
authentication and authenticated key exchange
Tri Van Le, Mike Burmester, Breno de Medeiros
March 2007 **Proceedings of the 2nd ACM symposium on Information, computer and communications security ASIACCS '07**
Publisher: ACM Press
Full text available: 📄 pdf(367.90 KB)   Additional Information: full citation, abstract, references, index terms

> Recently, a universally composable framework for RFID authentication protocols providing availability, anonymity, and authenticity was proposed. In this paper we extend that framework to address forward-security issues in the presence of key compromise. We also introduce new, provably secure, and highly practical protocols for anonymous authentication and key-exchange by RFID devices. The new protocols are lightweight, requiring only a pseudo-random bit generator. The new protocols satisfy forwar ...

> **Keywords**: RFID authentication and key-exchange protocols, anonymity, forward-security, universal composability

3 Web-based and Java-based simulation: Web-based simulation management: a web-based interface for storing and executing simulation models ▪
   Ashu Guru, Paul Savory, Robert Williams
   December 2000 **Proceedings of the 32nd conference on Winter simulation WSC '00**
   Publisher: Society for Computer Simulation International
   Full text available: 🔁 .pdf(181.99 KB)   Additional Information: full citation, abstract, references, citings

   > The dominance of the Internet in the development of information and communication technology has made Web-based distributed solutions increasingly attractive. Apart from providing other services, the World Wide Web is being looked upon as an environment for hosting modeling and simulation applications. SIMAN is a simulation language that allows users to simulate discrete and continuous systems. In this research, a web-based interface or toolkit has been developed for storing and executing SIMAN ...

4 Military applications: Security issues in high level architecture based distributed simulation ▪
   Asa Elkins, Jeffery W. Wilson, Denis Gracanin
   December 2001 **Proceedings of the 33nd conference on Winter simulation WSC '01**
   Publisher: IEEE Computer Society
   Full text available: 🔁 .pdf(262.26 KB)   Additional Information: full citation, abstract, references, index terms

   > The United States Department of Defense (DoD) has, over the past several years, emphasized the need to employ simulation based acquisition (SBA) in engineering and development. Distributed simulation introduces an information assurance challenge and details of a simulation must be guarded from unauthorized access. The High Level Architecture (HLA) and its Run-Time Interface (RTI) do not define support of mandatory access controls (MACs) or discretionary access controls (DACs) required to provide ...

5 Homeland security/emergency response: simulation for emergency management: Integrated simulation and gaming architecture for incident management training ▪
   Sanjay Jain, Charles R. McLean
   December 2005 **Proceedings of the 37th conference on Winter simulation WSC '05**
   Publisher: Winter Simulation Conference
   Full text available: 🔁 .pdf(249.50 KB)   Additional Information: full citation, abstract, references

   > The simulation-based training systems that are available or under development today for incident management are typically focused on macro level sequence of events. A few systems targeted at individual responders are under development using a gaming environment. Separate uses of such systems provide disparate experiences to decision makers and individual responders. There is a need to provide common training experiences to these groups for better effectiveness. This paper presents a novel approa ...

6 Formal modeling of active network nodes using PVS ▪
   Cindy Kong, P. Alexander, Darryl Dieckman
   August 2000 **Proceedings of the third workshop on Formal methods in software practice FMSP '00**
   Publisher: ACM Press
   Full text available: 🔁 .pdf(440.28 KB)   Additional Information: full citation, abstract, references, index terms

   > Active Networks are a new type of networks where all elements are programmable. Active packets can contain fragments of code to be executed on intermediate nodes they pass through. Active nodes provide the necessary environment and resources for the packets to be processed. In giving the users the capability to program the network as they desire, there is an issue of security risks. This paper presents a formal model for an active node that can be used to specify and verify the correct oper ...

7 **An optimally robust hybrid mix network**   ■
Markus Jakobsson, Ari Juels
August 2001 **Proceedings of the twentieth annual ACM symposium on Principles of**
distributed computing PODC '01
Publisher: ACM Press

Full text available: ⬛.pdf(858.02 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u>
<u>terms</u>

> We present a mix network that achieves efficient integration of public-key and symmetric-
> key operations. This *hybrid* mix network is capable of natural processing of arbitrarily long
> input elements, and is fast in both practical and asymptotic senses. While the overhead in
> the size of input elements is linear in the number of mix servers, it is quite small in
> practice. In contrast to previous hybrid constructions, ours has optimal robustness, that
> is, robustness against any minority coa ...

8 **Cryptographic protocols/ network security: Security proofs for an efficient password-**   ■
based key exchange
Emmanuel Bresson, Olivier Chevassut, David Pointcheval
October 2003 **Proceedings of the 10th ACM conference on Computer and**
communications security CCS '03
Publisher: ACM Press

Full text available: ⬛.pdf(233.51 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u>
<u>terms</u>

> Password-based key exchange schemes are designed to provide entities communicating
> over a public network, and sharing a (short) password only, with a session key (e.g, the
> key is used for data integrity and/or confidentiality). The focus of the present paper is on
> the analysis of very efficient schemes that have been proposed to the IEEE P1363
> Standard working group on password-based authenticated key-exchange methods, but
> which actual security was an open problem. We analyze the AuthA key excha ...
>
> **Keywords**: key exchange, password-based authentication

9 **Aurora: An Approach to High Throughput Parallel Simulation**   ■
Alfred Park, Richard M. Fujimoto
May 2006 **Proceedings of the 20th Workshop on Principles of Advanced and**
Distributed Simulation PADS '06
Publisher: IEEE Computer Society
Full text available: ⬛.pdf(333.08 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>index terms</u>

> A master/worker paradigm for executing large-scale parallel discrete event simulation
> programs over networkenabled computational resources is proposed and evaluated. In
> contrast to conventional approaches to parallel simulation, a client/server architecture is
> proposed where clients (workers) repeatedly download state vectors of logical processes
> and associated message data from a server (master), perform simulation computations
> locally at the client, and then return the results back to the serv ...

10 **Java-based query driven simulation environment**   ■
Rajesh S. Nair, John A. Miller, Zhiwei Zhang
November 1996 **Proceedings of the 28th conference on Winter simulation WSC '96**
Publisher: ACM Press, IEEE Computer Society
Full text available: ⬛.pdf(744.69 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>

> The concept of Web-based simulation can be finally realized using Java. We have used
> Java to create a powerful simulation modeling library which is based on the process

interaction paradigm. Java threads make it easy for us to implement each process (or active entity) as a thread. Our library, JSIM, supports both simulation and animation thus rendering the model developer's job easier. An important component of our approach is that we integrate our Java Simulator with a Database Management Syste ...

11 Computing curricula 2001

September 2001 **Journal on Educational Resources in Computing (JERIC)**
Publisher: ACM Press
Full text available: pdf(613.63 KB) html(2.78 KB)   Additional Information: full citation, references, citings, index terms

12 The Diesel Combustion Collaboratory: combustion researchers collaborating over the Internet
Carmen M. Pancerella, Larry A. Rahn, Christine L. Yang
January 1999 **Proceedings of the 1999 ACM/IEEE conference on Supercomputing (CDROM) Supercomputing '99**
Publisher: ACM Press
Full text available: pdf(8.95 MB)   Additional Information: full citation, references, citings, index terms

13 Agent-based modeling and simulation: Simulation using software agents I: linking spatially explicit parallel continuous and discrete models
Boleslaw K. Szymanski, Gilbert Chen
December 2000 **Proceedings of the 32nd conference on Winter simulation WSC '00**
Publisher: Society for Computer Simulation International
Full text available: pdf(223.71 KB)   Additional Information: full citation, abstract, references

This paper advocates the use of mobile agents for linking simulations running on different computers. A Mobile Component approach is proposed to enhance reusability of existing simulations and to improve efficiency of component based simulations of complex systems. A basic unit of the mobile component simulation is a simulation server with a communication interface to mobile agents. Each mobile agent links and coordinates component's execution. We used this approach to implement a combined Lyme ...

14 Web and IP based design: IP delivery for FPGAs using Applets and JHDL
Michael J. Wirthlin, Brian McMurtrey
June 2002 **Proceedings of the 39th conference on Design automation DAC '02**
Publisher: ACM Press
Full text available: pdf(198.07 KB)   Additional Information: full citation, abstract, references, index terms

This paper introduces an FPGA IP evaluation and delivery system that operates within Java applets. The use of such applets allows designers to create, evaluate, test, and obtain FPGA circuits directly within a web browser. Based on the JHDL design tool, these applets allow structural viewing, circuit simulation, and netlist generation of application-specific circuits. Applets can be customized to provide varying levels of IP visibility and functionality as needed by both customer and vendor.

Keywords: FPGA, JHDL, applet, intellectual property

15
Session 7A: New notions of security: achieving universal composability without trusted setup

Manoj Prabhakaran, Amit Sahai
June 2004 **Proceedings of the thirty-sixth annual ACM symposium on Theory of computing STOC '04**
Publisher: ACM Press

Full text available: ⬚ pdf(253.45 KB)   Additional Information: full citation, abstract, references, citings, index terms

We propose a modification to the framework of Universally Composable (UC) security [3]. Our new notion involves comparing the real protocol execution with an ideal execution involving ideal functionalities (just as in UC-security), but allowing the environment and adversary access to some super-polynomial computational power. We argue the meaningfulness of the new notion, which in particular subsumes many of the traditional notions of security. We generalize the Universal Composition theorem of ...

Keywords: environmental security, general composition, generalized environmental security, secure multi-party computation, secure protocols, simulation, universal composability

16 Three phase simulation in Java
Michael Pidd, R. A. Cassel
December 1998 **Proceedings of the 30th conference on Winter simulation WSC '98**
Publisher: IEEE Computer Society Press
Full text available: ⬚ pdf(48.10 KB)   Additional Information: full citation, references, citings, index terms

17 Reprint: MSIS 2006: model curriculum and guidelines for graduate degree programs in information systems
John T. Gorgone, Paul Gray, Edward A. Stohr, Joseph S. Valacich, Rolf T. Wigand
June 2006 **ACM SIGCSE Bulletin**, Volume 38 Issue 2
Publisher: ACM Press
Full text available: ⬚ pdf(868.32 KB)   Additional Information: full citation, abstract, index terms

This article presents the MSIS 2006 Model Curriculum and Guidelines for Graduate Degree Programs in Information Systems. As with MSIS 2000 and its predecessors, the objective is to create a model for schools designing or revising an MS curriculum in Information Systems. The curriculum was designed by a joint committee of the Association for Information Systems and the Association for Computing Machinery.MSIS2006 is a major update of MSIS 2000. Features include increasing the number of required c ...

Keywords: MS career tracks, MS course outlines, MS curriculum

18 General applications: Complex and interconnected systems: optimistic parallel simulation of a large-scale view storage system
Garrett Yaun, Christopher D. Carothers, Sibel Adali, David Spooner
December 2001 **Proceedings of the 33nd conference on Winter simulation WSC '01**
Publisher: IEEE Computer Society
Full text available: ⬚ pdf(139.73 KB)   Additional Information: full citation, abstract, references, index terms

In this paper we present the design and implementation of a complex view storage system model that is suitable for execution on a optimistic parallel simulation engine. What is unique over other optimistic systems is that reverse computation as opposed to state-saving is used to support the rollback mechanism. In this model, a hierarchy of view storage servers are connected to an array of client-side local disks. The term *view* refers to the output or result of a query made on the part of ...

19 Advances in Model-Based Testing (A-MOST 2005): A simulation model of a multi-server EJB system

David Mc Guinness, Liam Murphy

May 2005 **ACM SIGSOFT Software Engineering Notes , Proceedings of the first international workshop on Advances in model-based testing A-MOST '05,** Volume 30 Issue 4

Publisher: ACM Press

Full text available: .pdf(161.42 KB)   Additional Information: full citation, abstract, references, index terms

> Despite the fact that EJB (Enterprise Java Beans) is a widely used technology, research in the area of performance modelling of EJB application servers is quite sparse. This paper will describe how Workbench™, an advanced simulation modelling tool, can be used to build a scalable model of a multi-server EJB system that allows users to input variables that describe interactions and their constituent methods, as well as system parameters. The model will output the average time for each given ...

20 Computer security: Software security vulnerability testing in hostile environments

Herbert H. Thompson, James A. Whittaker, Florence E. Mottay

March 2002 **Proceedings of the 2002 ACM symposium on Applied computing SAC '02**

Publisher: ACM Press

Full text available: .pdf(547.79 KB)   Additional Information: full citation, abstract, references, citings, index terms

> Traditional Black box software testing can be effective at exposing some classes of software failures. Security class failures, however, do not tend to manifest readily using these techniques. The problem is that many security failures occur in stressed environments, which appear in the field, but are often neglected during testing because of the difficulty to simulate these conditions. Software can only be considered secure if it behaves securely under all operating environments. Hostile enviro ...

Keywords: fault injection, software defect, software failure, software security, software testing

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M Liljenstam, J Liu, D Nicol, Y Yuan, G Yan, C ... - Proceedings of the 19th ACM/IEEE/SCS Workshop on Principles ..., 2005 - doi.ieeecomputersociety.org
... tion with a focus on **security** for exercises ... thus alleviates the load on the **server** at the ... accelerating or decel- erating the **simulation execution** with respect ...
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A Elkins, JW Wilson, D Gracanin - Proceedings of the 33nd conference on Winter simulation, 2001 - portal.acm.org
... was created on the workstation to request **security** between the ... Table 1 provides **simulation** times for separate and si- multaneous **execution** of federates ...
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JH Saunders - ... for Information System **Security** Education, Redmond, WA, 2002 - cisse.info
... utilizing these tools in the **security** arena requires ... to utilizing Network Modeling and **Simulation** Packages. ... built-in" representation of the software **execution**. ...
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SD Chi, JS Park, KC Jung, JS Lee - Lecture Notes in Computer Science, 2001 - Springer
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command, output represents the results by command **execution**, and post ...
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... files with some busy-work in between, **simulating** a script ... ULTRIX that can confine
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W Cai, SJ Turner, H Zhao - Distributed Simulation and Real-Time Applications, 2002. ...,
2002 - ieeexplore.ieee.org
... Grid **Security** Infrastructure (GSI) provides sevices for ... Table 2. **Execution** Loop in
a Federate import lms.LMSHandler; ... ... main **simulation** loop for (int ...
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Web-based simulation management: a web-based interface for storing and executing simulation models - all 4 versions »
A Guru, P Savory, R Williams - Proceedings of the 32nd conference on Winter simulation,
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... To increase the system's functionality, **security**, and to ... results of compilation,
linking or **execution** (Figure 3 ... can interactively debug the **simulation** program ...
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P Michiardi, R Molva - European Wireless Conference, 2002 - eurecom.fr
... taken, the **simulation** results showed that network operation and ... that only forces the correct **execution** of the ... rising any suspects in the **security** mechanism. ...
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R McNab, FW Howell - Proceedings of the Twelfth UK Computer and ..., 1996 - dcs.ed.ac.uk
... This is a messy solution which breaks the built in **security** mechanisms, and is not really for the novice or casual browser. ... Table 1: **Simulation execution** times ...
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R David, SH Son, R Mukkamala - Proceedings of the 1995 IEEE Symposium on **Security** and ..., 1995 - doi.ieeecomputersociety.org
... Delay **security** is also violated, since the presence of TI delays both Tz and Ts ... the se- cure two-phase locking protocol is to try to **simulate execution** of Basic ...
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